



Index



## A

AC, 126  
 Access time, 463  
 Accumulator, 126  
 Accumulator register, 128–30  
   design of, 164–65  
 Addend, 338  
 Adder, 103–4  
 Adder and logic circuit, 130, 166  
 Adder-subtractor, 104–5  
 Addition algorithms:  
   decimal numbers, 365–68, 371  
   floating-point, 358–60  
   signed-magnitude, 335–38  
   signed-2's complement, 79, 338–40  
 Add microoperation, 102–4  
 Add-overflow, 336  
 Address, 60, 131  
 Address bus, 417–19  
 Address field, 226  
 Addressing modes, 260  
   autoincrement, 262  
   base register, 264  
   direct, 126–27, 263  
   immediate, 126, 262  
   implied, 261  
   indexed, 263–64  
   indirect, 126–27, 263  
   register, 262  
   register indirect, 262  
   relative, 263  
 Address mapping, 472  
 Address register, 128–30  
 Address sequencing, 216  
 Address space, 470  
 Address symbol table, 183  
 Adjacent squares, 12  
 Algorithm, 334  
 Alphanumeric code, 73, 86, 383  
 ALU, 116–17, 241–45  
 AND, 4  
 AND gate, 5

AND microoperation, 110–11  
   special symbol, 109  
 AR (*see* Address register)  
 Arbitration, 500  
 Arbitration algorithms, 505  
 Arbitration switch, 513  
 Architecture, 3  
 Arithmetic algorithm, 334  
 Arithmetic circuit, 106–8  
 Arithmetic instructions, 269–70  
 Arithmetic logic unit, 116, 244–45  
 Arithmetic microoperations, 102–3  
 Arithmetic pipeline, 307–10  
 Arithmetic processor, 333  
 Arithmetic shift, 114–15, 272, 346  
   overflow, 115  
 Array multiplier, 346–48  
 Array processor, 326  
   attached, 326–27  
   SIMD, 327–28  
 ASCII, 74–75, 184, 383–85  
 Assembler, 182, 185–90  
 Assembly language, 179–81  
 Associative memory, 456  
   hardware organization, 457–60  
   write-back, 469  
   write-through, 469  
 Associative mapping, 464  
 Asynchronous bus, 501  
 Asynchronous communication, 398–400  
 Asynchronous transfer, 391, 396  
 Attached array processor, 326–27  
 Autodecrement, 262  
 Autoincrement, 262  
 Auxiliary memory, 445, 452–56

## B

Base of a number, 68  
 Base register, 264  
 Basic computer, 123  
   common bus, 129–31

- Basic computer (*cont.*)
    - control, 135-39
    - design of, 157-67
    - flowchart, 158
    - input-output, 150-53
    - instruction cycle, 139, 142, 158
    - instructions, 132-34, 174-75
    - interrupt, 153-56
    - phases, 139
    - registers, 128, 157
    - tabular summary, 159
  - Baud rate, 398
  - BCD, 72-73, 81-82
  - BCD adder, 365-68
  - BCD subtractor, 368-69
  - Berkeley RISC I, 288-91
  - Biased exponent, 356
  - Bidirectional shift register, 53-56
  - Binary, 1
  - Binary adder, 103-4
  - Binary-coded decimal, 73, 85
  - Binary codes, 72, 84
    - alphanumeric, 73
    - ASCII, 74-75
    - BCD, 72-73
    - error detection, 87
    - excess-3, 85-86
    - Gray, 84
  - Binary counter, 56-58
    - design of, 32-36
    - with parallel load, 58-59
  - Binary incrementer, 105-6
  - Binary  $n$ -cube, 498
  - Binary numbers, 2, 68
  - Binary parallel adder, 103-45
  - Binary point, 77
  - Binary signal, 4
  - Binary tree, 497
  - Binary variable, 7, 213
  - Bit, 1
  - Bit-clear, 270
  - Bit complement, 270
  - Bit manipulation instructions, 270-72
  - Bit-oriented protocol, 437-39
  - Bit-set, 271
  - Block, 472
  - Block diagram, 18
  - Block transfer, 431
  - Boolean algebra, 7-11
  - Boolean expression, 8
  - Boolean function, 7
  - Booth algorithm, 343-46
  - Bootstrap loader, 448
  - Branch, 147, 275
  - Branch conditions, 276
  - Branch instructions, 273-76, 314-15
  - Branch logic, 217
  - Branch target buffer, 315
  - Buffer gate, 5, 100
  - Burst transfer, 416
  - Bus, 97-98, 500
    - of basic computer, 129-31
    - construction of, 98-101
    - input-output, 386
    - memory, 453
  - Bus arbitration, 502-6
  - Bus grant, 416
  - Bus organization, 242-44
  - Bus request, 416
  - Bus selection, 99
  - Bus transfer, 99
  - Byte, 60
- ## C
- Cache coherence, 509-12
  - Cache memory, 446, 462-69
  - Call subroutine, 200
  - CAM, 456
  - CAR (*see* Control address register)
  - Carry, 19, 76
  - Carry status bit, 274
  - Central processing unit, 241
  - Channel, 423
  - Character code, 73
  - Character manipulation, 204
  - Character-oriented protocol, 432
  - Characteristic table, 23-25
  - Chip, 41, 449
  - Chip select, 449
  - CISC, 282
  - Circular shift, 114
  - Clear, 23, 113
  - Clock, 22, 26, 96
  - Clock cycle, 137, 305
  - Clocked sequential circuit, 22
  - CMOS, 43
  - Combinational circuit, 18
    - analysis, 19
    - design, 19
  - Combinational circuit shifter, 116
  - Command, 387, 421
  - Comment field, 179
  - Common bus, 97-98, 129-31, 242-44
  - Communication control characters, 432
  - Compiler, 190
  - Compiler support, 316
  - Complement, 10, 74-76
    - subtraction with, 76
  - Complementer, 336-37
  - Complex instruction set computer, 282-84
  - Computer architecture, 3
  - Computer arithmetic, 333
  - Computer design, 3, 157-67
  - Computer hardware, 1
  - Computer instruction cycle, 139
  - Computer instructions, 132-34, 175

Computer network, 489  
 Computer organization, 3  
 Computer registers, 128  
 Computer software, 2, 173  
 Condition code, 274  
 Conditional branch, 217, 275-76  
 Content addressable memory, 456  
 Control address register, 215, 217  
 Control command, 387  
 Control design, 160-64, 231-32  
 Control function, 96  
 Control logic gates, 160  
 Control memory, 213-14, 229  
 Control signal, 136-38  
 Control unit, 135-39, 231  
 Control word, 213, 243-45  
 Counter, 56-58  
   design of, 32-36  
 CPU, 3, 241  
 CRC, 431  
 Critical section, 508  
 Crossbar switch, 494-96  
 Cycle stealing, 416  
 Cyclic redundancy check, 431

## D

Daisy-chain, 408, 502-3  
 Data bus, 417-19  
 Data communication, 429  
 Data communication protocol, 431  
   bit-oriented, 437-39  
   character-oriented, 432-33  
   example, 433-36  
   message format, 433, 437  
 Data dependency, 313-14  
 Data link, 431  
 Data manipulation instructions, 268-72  
 Data register, 128-30  
 Data representation, 67  
 Data selector, 49  
 Data set, 430  
 Data stream, 301  
 Data transfer instructions, 267-68  
 Data transparency, 436  
 Data types, 67, 269  
 Decimal adder, 365-68  
 Decimal arithmetic, 369-76  
   addition and subtraction, 371  
   division, 374-76  
   multiplication, 371-73  
 Decimal arithmetic unit, 363-69  
 Decimal codes, 85  
 Decimal numbers, 68, 81  
 Decimal representation, 81  
 Decimal subtraction, 368-69  
 Decode phase, 139, 226  
 Decoder, 43

Decrement, 108  
 Delayed branch, 315, 318-19  
 Delayed load, 314, 317-18  
 DeMorgan theorem, 8-10  
 Descriptor, 482  
 Design of basic computer, 157-67  
   Device, 382  
   D flip-flop, 23  
   Digital, 1  
   Digital computer, 1  
   Digital integrated circuits, 41  
   Digital logic families, 42  
   Digital signal, 4  
   Digital system, 1  
 Direct address, 126-27, 263  
 Direct mapping, 465  
 Direct memory access, 415-20  
   controller, 416-18  
   transfer, 418-20  
 Disk, 383, 454  
 Distributed memory, 491  
 Distributed switch, 513  
 Divide overflow, 351  
 Dividend, 349  
 Dividend alignment, 362  
 Division algorithms:  
   comparison method, 353  
   decimal numbers, 374-76  
   floating-point, 362-63  
   non-restoring, 353-54  
   restoring method, 353  
   signed-magnitude, 349-53  
 Divisor, 349  
 DLE character, 436  
 DMA (see Direct memory access)  
 Don't-care conditions, 16  
 Double-precision, 196  
 DR (see Data register)  
 Drum, 454  
 Dual-bus, 492-93  
 Dynamic arbitration, 505  
 Dynamic input, 22-23  
 Dynamic microprogramming, 214  
 Dynamic relocation, 476

## E

EBCDIC, 87  
 ECL, 43  
 Edge triggered flip-flop, 25  
 EEPROM, 63  
 Effective address, 126, 262  
 Emitter-coupled logic, 43  
 Enable input, 44  
 Encoder, 47  
 End carry, 76  
 EPROM, 63  
 Equivalence, 5

Error detection code, 87  
 Error diagnostics, 188  
 Excess-3 code, 85-86  
 Excitation table, 27, 33  
 Exclusive-NOR, 5  
 Exclusive-OR, 6, 111  
 Execute phase, 139, 228  
 Exponent, 83, 354  
 External interrupt, 281

## F

FA (*see* Full-adder)  
 Fetch phase, 139-41, 156, 226  
 FIFO, 400, 476  
 FIFO buffer, 400-2, 505  
 First-in first-out, 400-2  
 Fixed-point, 77  
   binary representation, 79-80  
   decimal representation, 81-82  
 Flag, 152, 437  
 Flag bits, 274  
 Flip-flop, 22-28  
   asynchronous clear, 26  
   edge-triggered, 25  
   excitation table, 27  
   IC, 26  
   input function, 28  
   master-slave, 26  
   types of, 22-25  
 Floating-point, 83, 354-57  
   arithmetic operations, 358-63  
   normalization, 83, 355  
 Floating-point pipeline, 308-10  
 Floppy disk, 455  
 Flops, 325  
 Flowchart for basic computer, 158  
 Fortran, 178  
 Full-adder, 20-22, 104  
 Full-duplex, 431

## G

Gates, 4-6  
 General register organization, 242-47  
 Graphic symbols, 23  
 Gray code, 84

## H

Half-adder, 19  
 Half-duplex, 431  
 Handshaking, 391, 393-96  
 Hardware, 2, 173  
 Hardware algorithm, 334  
 Hardware interlock, 312  
 Hardware lock, 508

Hardwired control, 136, 216  
 HDLC, 437  
 Hexadecimal code, 134, 177  
 Hexadecimal numbers, 68-70  
   binary coded, 71  
   conversion to binary, 70  
 High-impedance, 100  
 Hit ratio, 463  
 Hold time, 26  
 Hypercube, 498-500

## I

IBM channel, 423-26  
 IC (*see* Integrated circuit)  
 Immediate mode, 126, 262  
 Implied mode, 261  
 Increment, 58, 103, 108  
 Incrementer, 105-6  
 Index addressing, 263-64  
 Indirect address, 126-27, 132, 263  
 Infix notation, 252  
 Inner product, 322  
 Input carry, 106  
 Input equation, 28  
 Input flag, 152  
 Input logic, 233-35  
 Input-output, 150-53, 381  
   bus, 386, 421  
   devices, 381-83  
   instructions, 152  
   interface, 385-87, 389-91  
   programming, 203  
   transfer, 391-98  
 Input-output processor, 420-22  
 Instruction code, 123, 132  
 Instruction cycle, 139, 310-11  
 Instruction format, 255, 322  
 Instruction register, 128  
 Instruction pipeline, 310-15  
 Instruction set completeness, 134-35  
 Instruction stream, 301  
 Instructions, 133, 175  
 Integrated circuit, 41-43  
 Intel-8089 IOP, 427  
 Interchange switch, 496  
 Interconnection of processors, 491-500  
 Interface, 385, 389  
 Interleaved memory, 324  
 Internal interrupt, 281  
 Interprocessor arbitration, 500  
 Interprocessor communication, 506  
 Interprocessor synchronization, 507  
 Interregister transfer, 95  
 Interrupt, 153-56, 281, 407-12  
   priority, 407  
   types of, 281-82  
   vectored, 406

Interrupt acknowledge, 410  
 Interrupt cycle, 153-56, 412-13  
 Interrupt initiated I/O, 46  
 Interrupt program, 205-8  
 Interrupt register, 411  
 Interrupt request, 417  
 Inverter gate, 5  
 IR (*see* Instruction register)  
 I/O, 381  
 I/O bus, 421  
 I/O instructions, 152  
 I/O interface, 385-87  
 I/O port, 389  
 I/O software, 406, 413  
 IOP (*see* Input-output processor)  
 Isolated I/O, 388

## J

JK flip-flop, 24  
 Jump, 225, 273

## K

Karnaugh map, 11  
 Keyboard, 382

## L

Label, 179, 225  
 Large-scale integration, 42  
 Last-in first-out, 247  
 LD (*see* Load control)  
 Least recently used, 476  
 LIFO, 247  
 Load control, 52, 129  
 Load input, 129-31  
 Local bus, 492-93  
 Locality of reference, 462  
 Location counter, 185  
 Lock mechanism, 508  
 Logic circuit, 111  
 Logic circuit families, 42  
 Logic diagram, 7  
 Logic gates, 4  
 Logic microoperations, 108-10  
   hardware implementation, 111  
   list of, 109-10  
 Logic operations, 197  
 Logical address, 477  
 Logical instructions, 270-72  
 Logical shift, 114, 272  
 Loosely coupled multiprocessors, 491  
 LRC, 431  
 LRU, 476, 505  
 LSI, 42

## M

M (*see* Memory word)  
 Machine language, 174-75  
 Magnetic disk, 383, 454  
 Magnetic drum, 383  
 Magnetic tape, 383, 455  
 Magnitude, 335  
 Magnitude comparator, 336  
 Main memory, 445, 448  
 Mantissa, 83, 354  
 Map simplification, 11-18  
 Mapping, 216, 219, 463  
 Mask operation, 113  
 Mask register, 411  
 Master-slave flip-flop, 26  
 Match logic, 459  
 Matrix multiplication, 322  
 Medium-scale integration, 42  
 Memory:  
   access time, 463  
   associative, 456-62  
   auxiliary, 452-56  
   cache, 462-69  
   chip, 449  
   content addressable, 456  
   FIFO, 400  
   IC, 41, 449  
   random access, 60, 448  
   read only, 61, 450  
   virtual, 469  
 Memory address, 60, 131  
 Memory address map, 450-52  
 Memory array, 324  
 Memory bus, 387, 453  
 Memory cycle, 138  
 Memory hierarchy, 445  
 Memory interleaving, 324  
 Memory management, 476-79  
 Memory-mapped I/O, 388-89  
 Memory module, 494  
 Memory organization, 445  
 Memory page table, 473, 478  
 Memory protection, 482  
 Memory read, 61, 101  
 Memory reference instructions, 145-50  
 Memory space, 470  
 Memory stack, 249-51  
 Memory transfer, 101-2  
 Memory word, 58, 101  
 Memory write, 60-61, 101-2  
 Message format, 433  
 Message-passing, 491  
 Message routing, 499  
 Metal-oxide semiconductor, 43  
 Microcomputer, 500  
 Microinstruction, 214  
   binary form, 229-30  
   formats, 222-24  
   symbolic form, 225-28

Microoperation, 93, 102, 223, 246  
   arithmetic, 102-3  
   logic, 108-10  
   read, 101  
   register transfer, 102  
   shift, 114-15  
   write, 102  
 Microprocessor, 490  
 Microprogram, 214  
   binary, 229-30  
   control, 231-32  
   example, 220-30  
   sequencer, 232-35  
   symbolic, 225-28  
 Microprogrammed control unit, 214  
 Minterm, 11  
 Minuend, 338  
 MIMD, 301, 489  
 Mode field, 261, 265  
 Modem, 430  
 Modes of transfer, 402-3  
 Monitor, 382  
 MOS, 43  
 Move instruction, 256  
 MSI, 42  
 Multiple functional unit, 300, 306  
 Multiplexer, 48-50, 98  
 Multiplicand, 340  
 Multiplication algorithms:  
   Booth algorithm, 343-46  
   decimal numbers, 371-73  
   floating-point, 360-62  
   signed-magnitude, 341-43  
   signed-2's complement, 343-46  
   software implementation, 195  
 Multiplication program, 193-96  
 Multiplier, 340  
 Multiport memory, 493  
 Multiprocessor, 489  
   arbitration, 500-6  
   communication, 506  
   interconnection, 491-500  
   synchronization, 507-9  
 Multiprogramming, 447  
 Multistage switching network, 496-98  
 Mutual exclusion, 508  
 MUX (see Multiplexer)

## N

NAND gate, 5, 9, 16  
 Next state, 23, 30  
 NOR gate, 5, 9, 16  
 Normalization, 83, 355  
 Number base conversion, 68-71  
 Number system, 68

## O

Object program, 183  
 Octal numbers, 68-72  
   binary coded, 70  
   conversion to binary, 70  
 Odd function, 6, 88  
 Omega network, 497-98  
 One-address instruction, 259  
 On-line, 382  
 Opcode, 125  
 Operating system, 447, 483, 506  
 Operation code, 124, 255  
 ORG, 180, 226  
 OR gate, 5-6  
 OR microoperation, 110-11  
   special symbol, 109  
 Output flag, 152  
 Overflow, 80-81, 115  
 Overflow status bit, 274  
 Overlap register window, 285-88

## P

Packet, 491  
 Page, 472  
 Page fault, 475  
 Page frame, 472  
 Page replacement, 475  
 Page table, 474  
 Parallel adder, 336-37  
 Parallel arbitration logic, 503-5  
 Parallel load, 54  
 Parallel processing, 299  
 Parallel transfer, 51  
 Parameter linkage, 200  
 Parity bit, 87  
 Parity checker, 87-89  
 Parity generator, 87-89  
 Partial product, 341, 343  
 Partial remainder, 349  
 PC (see Program counter)  
 Peripheral, 382  
 Pipeline, 302-7  
   arithmetic, 307-10  
   in RISC, 315-19  
   instruction, 310-15  
 Pipeline conflicts, 313  
 Pipeline processing, 299  
 Pointer, 192  
 Polish notation, 252  
 Polling, 407, 505  
 Pop stack, 249  
 Port, 389  
 Present state, 23, 30  
 Printer, 382  
 Priority encoder, 411-12



- Priority interrupt, 407-12
    - daisy-chaining, 408-9
    - parallel, 409-11
    - software routines, 413-15
  - Priority logic, 410
  - Product, 340
  - Product of sums, 14
  - Program, 2, 173
  - Program control instructions, 273-74
  - Program counter, 128-30
  - Program interrupt, 153-56, 205, 279
  - Program loop, 190
  - Program status word, 280
  - Programmed I/O, 402-6
  - Programming languages, 176
  - PROM, 63
  - Protection, 482
  - Protocol, 431
  - Pseudo-instruction, 178-80
  - Push stack, 247-49
  - PSW, 280
- R**
- Radix, 68
  - RAM, 60, 448
  - Random-access memory, 60-61, 448-50
  - Read, 60-61, 101
  - Read microoperation, 101
  - Read only memory, 61-63, 448
    - programming of, 62-63
    - truth table, 62
    - types of, 62-63
  - Reduced instruction set computer, 282-85
    - characteristics, 284-85
    - instructions, 259-60, 288-89
    - pipeline, 315-19
  - Register, 50, 95
  - Register address, 255
  - Register indirect mode, 262
  - Register load, 50
  - Register mode, 262
  - Register with parallel load, 51
  - Register-reference instructions, 143-44
  - Register stack, 247-49
  - Register transfer, 95
  - Register transfer language, 94
    - basic symbols, 97
  - Register transfer statements, 95
    - arithmetic, 102-3
    - conditional control, 96
    - logic, 108-10
    - shift, 114
  - Registers of basic computer, 128, 157
  - Relative addressing, 263
  - Remainder, 349-50
  - Replacement algorithms, 475-76
  - Resource conflict, 313
  - Return address, 148
  - Return from subroutine, 278
  - Reverse Polish notation, 251-54
    - arithmetic expressions, 253-54
  - RISC, 282-85
    - instructions, 259-60, 288-91
    - pipeline, 315-19
  - ROM, 61, 214, 230, 450
  - Rotating daisy-chain, 505
  - Routine, 216
  - RPN, 252
- S**
- Schottky TTL, 42
  - SDLC, 437
  - Sector, 455
  - Segment, 304, 477
  - Segment descriptor, 482
  - Selection inputs, 48
  - Selective-clear, 112
  - Selective-complement, 112
  - Selective-set, 112
  - Self-complementing code, 86
  - Semaphore, 508
  - Sequence counter, 341
  - Sequencer, 215, 232-35
  - Sequential circuit, 28
    - design of, 32-36
  - Serial communication, 429
  - Serial communication interface, 151
  - Serial input, 53
  - Service program, 414
  - Service routine, 207
  - Set, 23
  - Set-associative mapping, 467
  - Setup time, 26
  - Shared memory system, 491-92
  - Shifter, 115-16
  - Shift instructions, 371-72
  - Shift microoperations, 114
    - hardware implementation, 115-16
  - Shift operations, 197
  - Shift register, 53
  - Signal, 4
  - Sign status bit, 274
  - Signed-magnitude, 78
  - Signed-2's complement, 78
    - overflow, 86
  - Signed numbers, 78
  - SIMD, 301, 327
  - Simplex, 431
  - Single-cycle execution, 316
  - SISD, 301
  - Small-scale integration, 41
  - Snoopy cach controller, 512
  - Software, 2, 173, 406, 413
  - Software interrupt, 281-82

- Source program, 183
  - SP (see Stack pointer)
  - Space-time diagram, 304-5
  - Speedup ratio, 305-6
  - SR flip-flop, 22
  - SSI, 41
  - Stack, 247
  - Stack instructions, 259
  - Stack limit, 251
  - Stack operations, 254
  - Stack organization, 247-49
  - Stack pointer, 247
  - Start bit, 396
  - State diagram, 31-32
  - State table, 30-31
  - Status bits, 217, 274
  - Status command, 387
  - Status register, 404
  - Stop bit, 397
  - Store, 147
  - Stored program organization, 125
  - Strobe control, 391
  - Subroutine, 198, 278-79
  - Subroutine call, 148, 200, 278
  - Subroutine parameters, 200
  - Subroutine program, 199
  - Subroutine register, 220
  - Subroutine return, 200, 278
  - Subtraction algorithms:
    - decimal numbers, 368-69, 371
    - floating-point, 358-60
    - signed-magnitude, 335-38
    - signed-2's complement, 89, 338-40
    - unsigned numbers, 76
  - Subtract microoperation, 102-5
  - Subtractor, 104-5
  - Subtrahend, 338
  - Sum of minterms, 11
  - Sum of products, 14
  - Supercomputer, 325
  - Supervisor mode, 280
  - Symbol table, 183
  - Symbolic address, 179
  - Symbolic microprogram, 227
  - Symbolic program, 175, 184
  - SYN character, 432
  - Synchronization, 507
  - Synchronous bus, 501
  - Synchronous clear, 58
  - Synchronous transfer, 396
  - Synchronous transmission, 430
  - System bus, 493, 500-2
- T**
- Table-lookup, 187
  - Tag field, 465
  - Tape, 383, 455
  - Task, 304
  - Test and set, 508
  - T flip-flop, 24
  - Three-address instruction, 258
  - Three-state buffer, 100
  - Three-state bus, 100-1
  - Throughput, 299
  - Tightly coupled multiprocessor, 491
  - Time shared bus, 491-93
  - Time slice, 505
  - Timeout, 396
  - Timing and control, 135
  - Timing diagram, 26, 391
  - Timing signal, 136-38
  - TLB, 479
  - Track, 455
  - Transistor-transistor logic, 42-43
  - Translation look-aside buffer, 479
  - Trap, 281
  - Truth table, 4, 7
  - TTL, 42
  - Two-address instruction, 258
- U**
- UART, 398
  - Underflow, 356
  - Unsigned numbers, 76
    - subtraction of, 76-77
- V**
- Valid bit, 469
  - Vector address, 408
  - Vector operations, 321
  - Vector processing, 319
  - Vectored interrupt, 406
  - Video monitor, 382
  - Virtual address, 470
  - Virtual memory, 469
  - VLSI, 42, 490
- W**
- Weighted code, 86
  - Word, 58
  - Word count register, 417
  - Write, 60-61, 101
  - Write microoperation, 102
  - Write-back, 469
  - Write-through, 469
- X**
- XOR (see Exclusive-OR)

Z

- Zero-address instruction, 259
- Zero detection, 275
- Zero insertion, 437
- Zero status bit, 274

